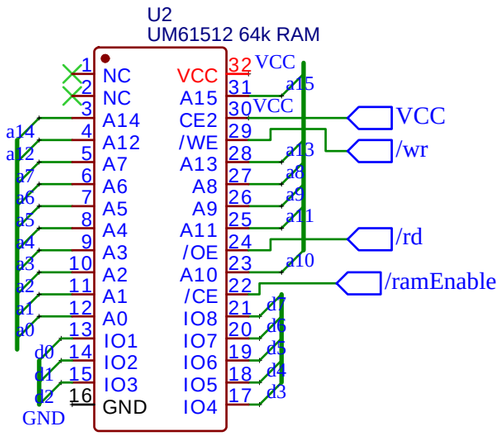
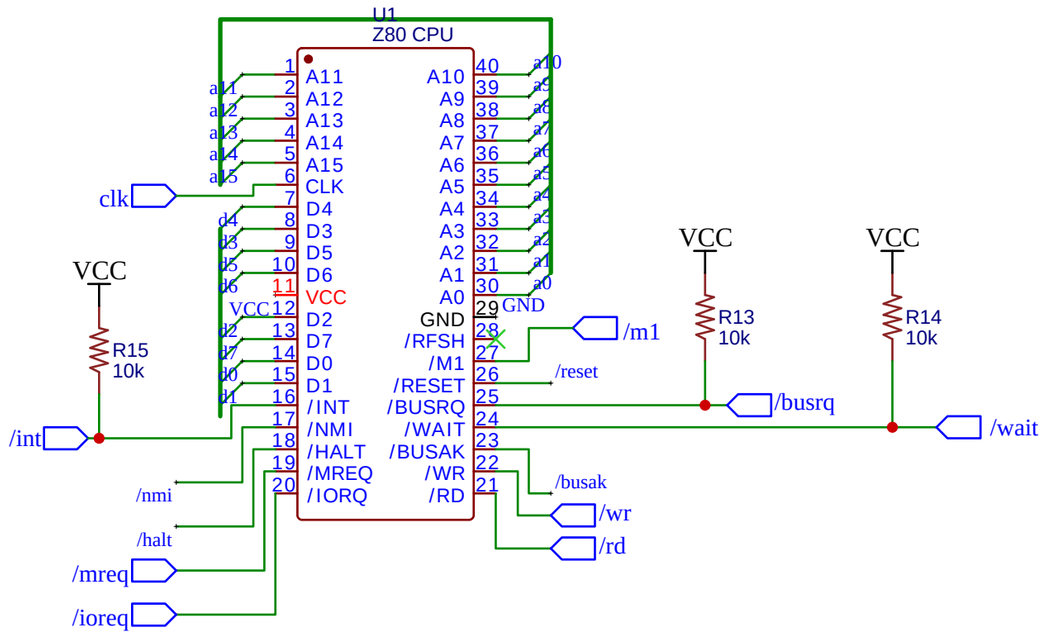
u1 u2 u3 u11

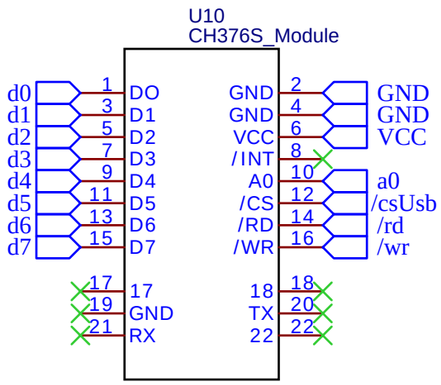
When /romEnable is low we want ROM.

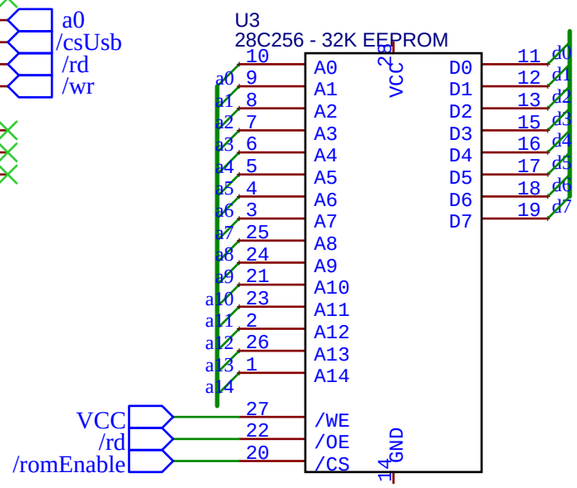
When /ramEnable is low we want RAM.

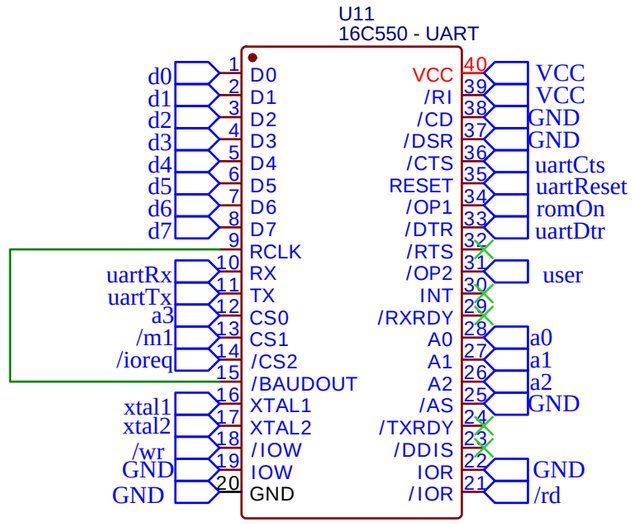




CH376S USB Pen-Drive Module







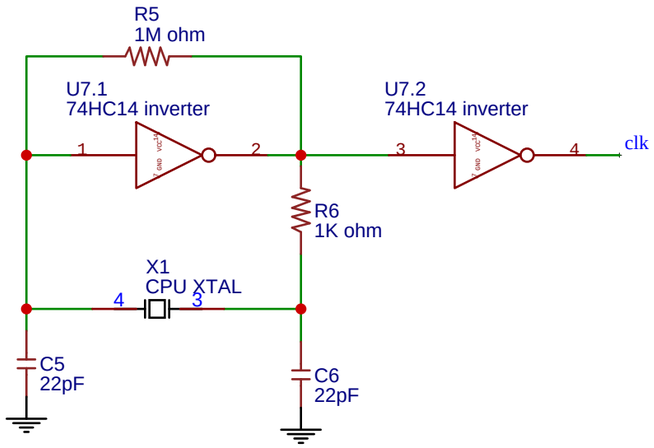
romON goes high on reset

NOTE: INT & RESET are positive-logic.

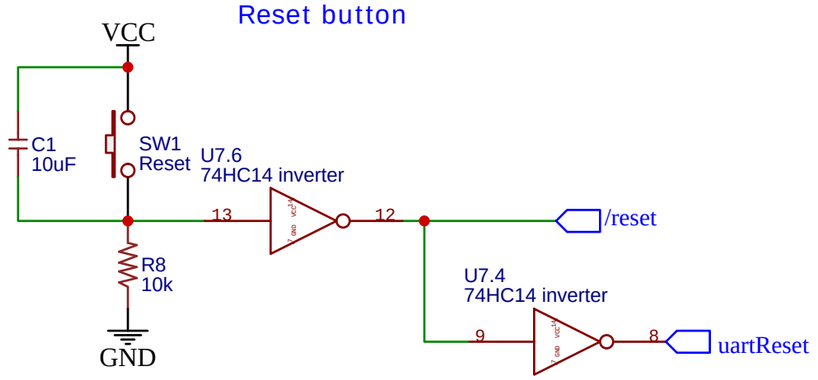
TITLE: Main chips		REV: 1.1
EasyEDA	Company: 8bitstack.co.uk	Sheet: 1/1
	Date: 2020-05-11 Drawn By: johnsquires	

u7 u8.1

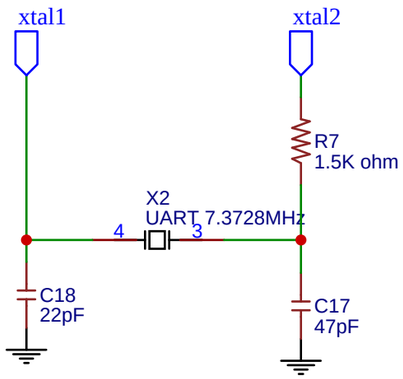
CPU clock - up to 20MHz crystal



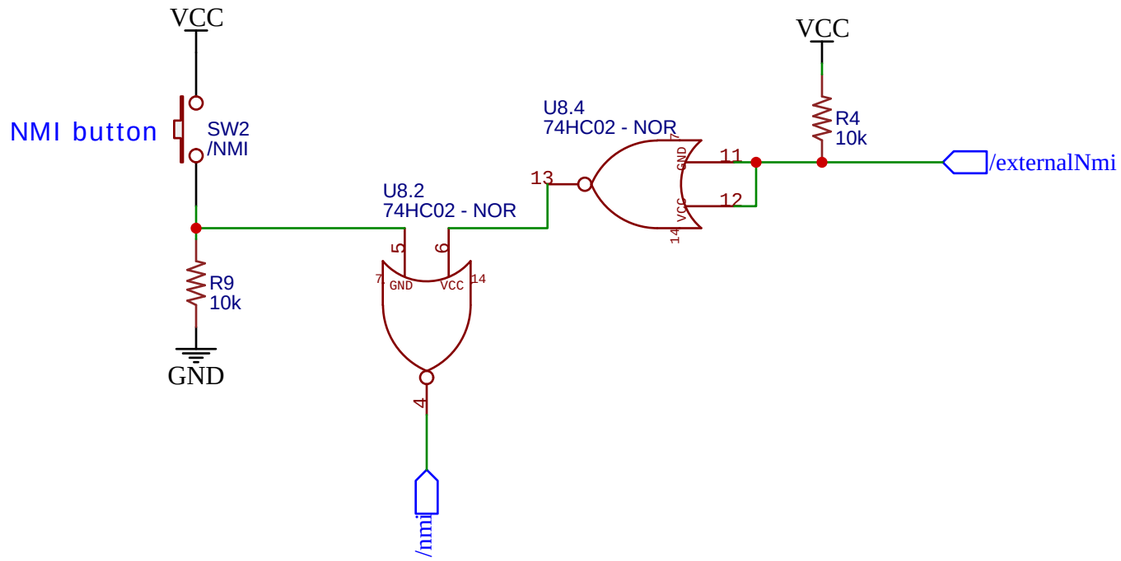
Reset button



UART clock - 1.8432 MHz



NMI button



TITLE: Clock and reset		REV: 1.0
EasyEDA	Company: 8bitstack.co.uk	Sheet: 1/1
	Date: 2020-05-11 Drawn By: johnsquires	

u5 u6

Assuming /MREQ is low, we want ROM when:

(1) A15 is low

(2) A14J is low (this is the jumpered version of a14)

(3) /FFJ is low (this is the jumpered version of the FF)

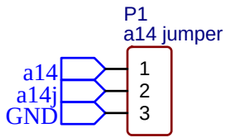
(4) /RD is low

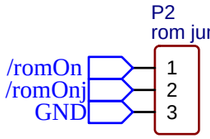
Otherwise we want RAM.

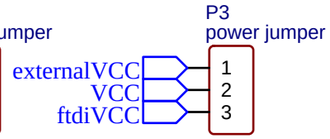
/wantROM = 1 or 2 or 3 or 4

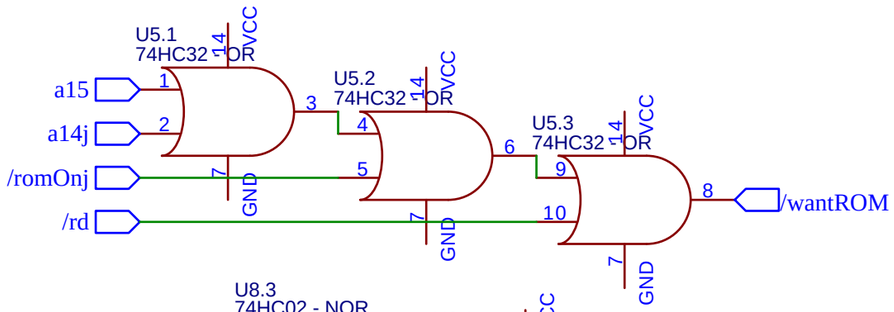
/romEnable = /MREQ or /wantROM

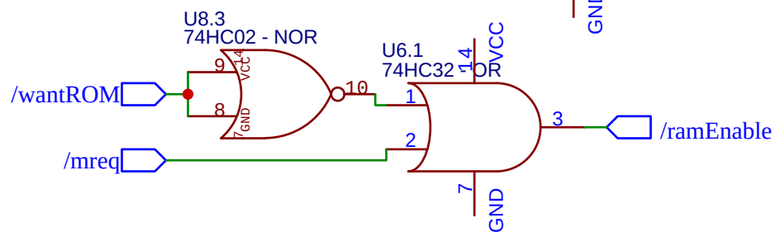
/ramEnable = /MREQ or NOT /wantROM

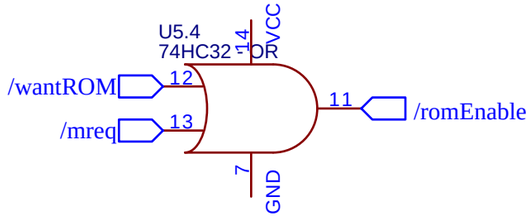


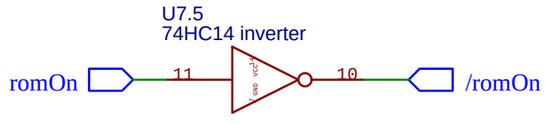


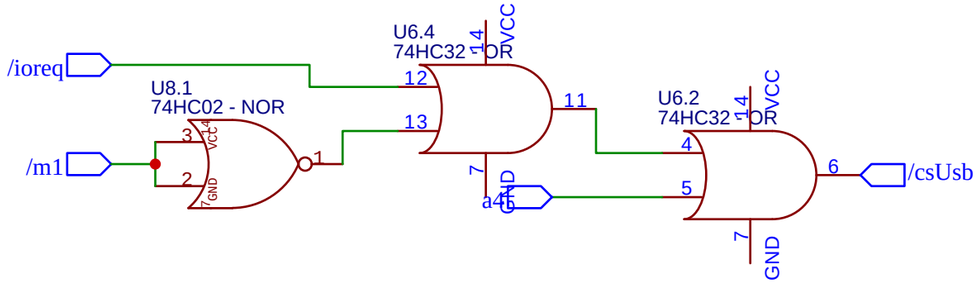


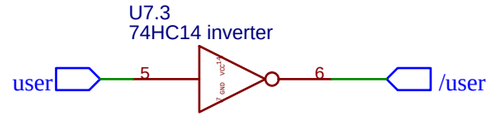












For I/O for UART we want:

(1) /IOREQ is low

(2) /M1 is high

(3) a3 is high

(4) a4 is high

base uart port = 00011000 = 24

For I/O for USB we want:

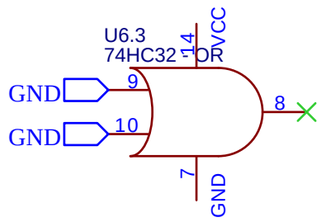
(1) /IOREQ is low

(2) /M1 is high

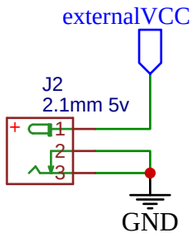
(3) a3 is low

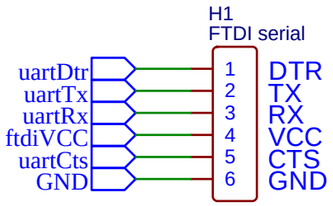
(4) a4 is low

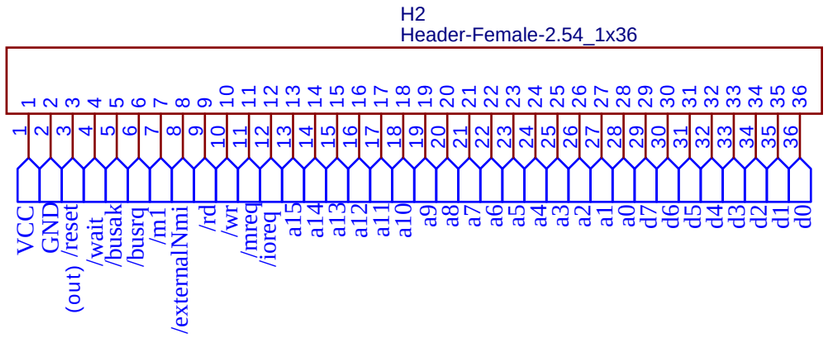
base USB port = 00000000 = 0

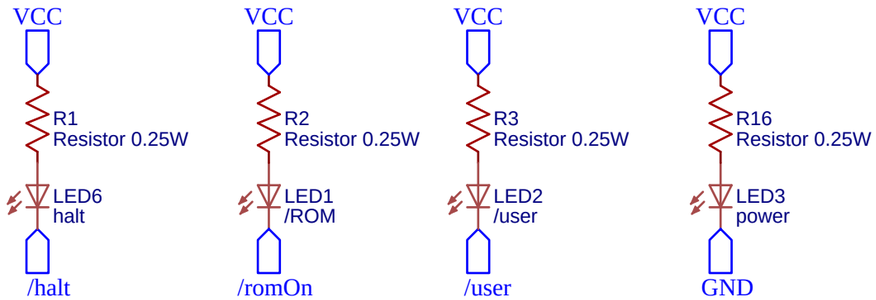


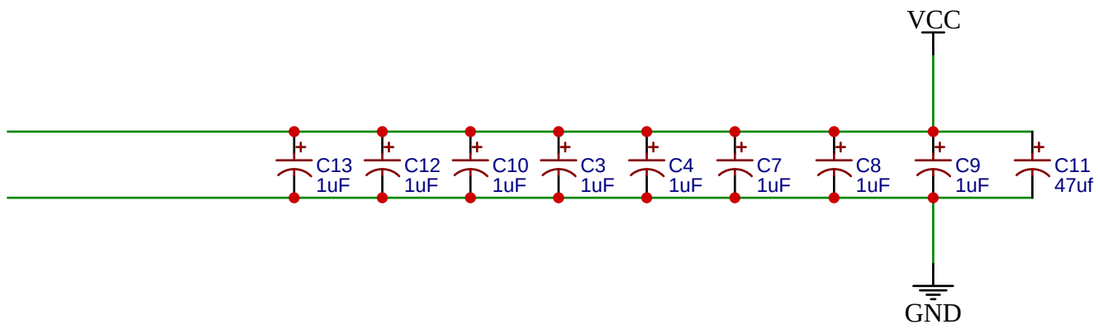
TITLE: ROM / RAM / IO select logic		REV: 1.0
EasyEDA	Company: 8bitstack.co.uk	Sheet: 1/1
	Date: 2020-06-11 Drawn By: johnsquires	











TITLE: Connectors		REV: 1.0
EasyEDA	Company: 8bitstack.co.uk	Sheet: 1/1
	Date: 2020-05-04 Drawn By: johnsquires	